

Translation of the attached sheet (Japanese text portions only)
Background Art Information

Patent No./Publication	Inventor(s)/Author(s)	Date etc
*Concise Explanation		
<p>Jpn. Pat. Appln. KOKAI Publication No. 9-237945; Published September 9, 1997; "PRINTED WIRING BOARD AND COMPONENT MOUNTING DEVICE"; Daikin Industries, Ltd.</p>		
*Concise Explanation This publication describes a method for identifying a wiring board using jumpers. The method is similar to that described in the "Background Art" section of the present invention.		
<p>Jpn. Pat. Appln. KOKAI Publication No. 7-282563; Published October 27, 1995; "Cassette"; TDK Corporation</p>		
*Concise Explanation This publication differs from the present invention, in that an electrical resistance circuit board which is to be identified and an object which is used for the identification purpose must be manufactured independently and then combined with each other.		
Prior Applications of Inventors or of Kabushiki Kaisha Toshiba (Assignee) Application No. Toshiba Reference Country Agent memo		
Inventor(s) Signature & Date		

Patent engineer's comment on inventor's information or patent engineer's information		
None *		
Checked by	Dated	
Toshiba Reference	Japanese Agent's Ref	sheet